

1. Listing of the claims:

1. (Currently Amended) A method of determining, from transform coded data, an inverse transform to generate a the number of bits required to represent an output value which would be obtained as a result of an inverse transform being performed on said transform coded data, said method comprising the steps of:

obtaining, at an MPEG decoder, a sum of coefficient values within said transform coded data (204); ~~and~~

comparing, in the MPEG decoder, this sum to a pre-determined threshold value ~~(206)~~;

deciding, in the MPEG decoder, as a consequence of said comparison which inverse transform implementation, out of a number of pre-determined implementations, should be performed when decoding said transform coded data; and

performing in the MPEG decoder on the transform coded data, the decided inverse transform.

2. (Original) A method as claimed in claim 1 wherein said transform coded data is discrete cosine transform (DCT) coded data

3. (Previously Presented) A method as claimed in claim 1 wherein said transform coded data is MPEG-1 or MPEG-2 encoded video data.

4. (Previously Presented) A method as claimed in claim 1 wherein said method is used to determine whether said output values can be represented in eight bits, or require nine bit representation.

5. Cancelled.

6. (Original) A method as claimed in claim 5 wherein at least one of said inverse transform implementations includes instructions for handling of multiple eight bit values in longer words.

7. (Previously Presented) A method as claimed in claim 1 wherein the coefficient values are bi-polar, and said sum is of the absolute values of the coefficients.

8. (Previously Presented) A method as claimed in claim 1 wherein the transform coded data consists of an 8x8 discrete cosine transform.

9. (Original) A method as claimed in claim 8 wherein said pre-determined threshold value is in the range 500 to 530.

10. (Currently Amended) Apparatus for determining, from transform coded data, an inverse transform to generate a ~~the~~ number of bits required to represent an output value which would be obtained as a result of an inverse transform being performed on said transform coded data, said apparatus comprising

means for obtaining a sum of coefficient values within said transform coded data

~~and~~ means for comparing this sum to a predetermined threshold value;

means for deciding as a consequence of said comparison which inverse transform implementation, out of a number of pre-determined implementations, should be performed when decoding said transform coded data; and

means for performing on the transform coded data, the decided inverse transform.

11. (Original) Apparatus as claimed in claim 10 wherein said transform coded data is discrete cosine transform (DCT) coded data.

12. (Previously Presented) Apparatus as claimed in claim 10 wherein said transform coded data is MPEG-1 or MPEG-2 encoded video data.

13. (Previously Presented) Apparatus as claimed in claim 10 wherein said apparatus is suitable for determining whether said output values can be represented in eight bits, or require nine bit representation.

14. Cancelled.

15. (Original) Apparatus as claimed in claim 14 wherein at least one of said inverse transform implementations includes instructions for handling of multiple eight bit values in longer words.

16. (Previously Presented) Apparatus as claimed in claim 10 wherein the coefficient values are bi-polar, and said sum is of the absolute values of the coefficients.

17. (Previously Presented) Apparatus as claimed in claim 10 wherein the transform coded data consists of an 8x8 discrete cosine transform.

18. (Original) Apparatus as claimed in claim 17 wherein said predetermined threshold value is in the range 500 to 530.

19. (Previously Presented) A record carrier wherein are recorded program instructions for causing a programmable processor to perform the steps of the method as claimed in claim 1.